



EXPRESS MAIL NO. EM035620999US

Sheet 1 of 1

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| U.S. DEPARTMENT OF COMMERCE<br>PATENT AND TRADEMARK OFFICE<br><br><b>INFORMATION DISCLOSURE STATEMENT</b><br>(Use several sheets if necessary) |  |                | ATTY. DOCKET NO.    | APPLICATION NO. |
|  |  |                | 852463.406          | 10/727,138      |
|  |  |                | APPLICANTS          |                 |
|  |  |                | Kaushik Saha et al. |                 |
| FILING DATE  |  | GROUP ART UNIT |                     |                 |
| December 3, 2003   |  | 2193           |                     |                 |

### U.S. PATENT DOCUMENTS

| *EXAMINER INITIAL |    | DOCUMENT NUMBER | DATE     | NAME         | CLASS | SUBCLASS | FILING DATE IF APPROPRIATE |
|-------------------|----|-----------------|----------|--------------|-------|----------|----------------------------|
|                   | AA | 6,618,800 B1    | 09/09/03 | Weiss et al. | 712   | 13       |                            |
|                   | AB | 6,751,643 B2    | 06/15/04 | Jaber        | 708   | 409      |                            |
|                   | AC | 6,907,439 B1    | 06/14/05 | Wicker       | 708   | 404      |                            |
|                   | AD | 2004/0236809 A1 | 11/25/04 | Saha et al.  | 708   | 404      |                            |

### FOREIGN PATENT DOCUMENTS

|  |    | DOCUMENT NUMBER | DATE     | COUNTRY | TRANSLATION |    |
|--|----|-----------------|----------|---------|-------------|----|
|  |    |                 |          |         | YES         | NO |
|  | AE | 01/69424 A2     | 09/20/01 | WIPO    |             |    |
|  | AF | 1426872 A2      | 06/09/04 | EP      |             |    |
|  | AG | 1447752 A2      | 08/18/04 | EP      |             |    |

### OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

|    |  |
|----|--|
| AH | Bhuyan et al., "Performance Analysis of FFT Algorithms on Multiprocessor Systems," IEEE Transactions on Software Engineering, SE-9(4):512-521, July 1983.  |
| AI | Chu et al., "Inside the FFT Black Box: Serial and Parallel Fast Fourier Transform Algorithms," CRC Press LLC, 2000, Chap. 8, "An In-Place Radix-2 DIT FFT for Input in Bit-Reversed Order," pp. 69-79; Chap. 17, "Parallelizing the FFTs: Preliminaries on Data Mapping," pp. 177-182; Section 19.2.3, Section 19.2.4, Section 19.3, pp. 209-210 and Section 21.1.3, p. 226. |
| AJ | Dawoud, D.S., "An Effective Memory Addressing Scheme For Multiprocessor FFT System," Proceedings of the 6 <sup>th</sup> IEEE Africon Conference, University of Pretoria, George, South Africa, October 2-4, 2002.  |
| AK | European Search Report, dated December 23, 2005, for EP 1 447 752, 3 pages.  |
| AL | Jones et al., "A Bus-Oriented Multiprocessor Fast Fourier Transform," IEEE Transactions on Signal Processing, 39(11):2547-2552, November 1991.   |
| AM | Taketa et al., "Radix R Parallel FFT algorithms with a global interconnection networks and Its Evaluation," Third International Symposium on Parallel Architectures, Algorithms and Networks, Taipei, December 18-20, 1997, pages 424-428.   |

| EXAMINER | DATE CONSIDERED |
|----------|-----------------|
|          |                 |

\* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant